



INFORMATION DISCLOSURE CITATION

PTO-1449

Complete If Known

Sheet	1	of	3	Application Number	10/742,327
				Filing Date	December 18, 2003
				First Named Inventor	Tsern et al.
				Art Unit	3694
				Examiner Name	Cheung, Mary Da Zhi Wang
				Attorney Docket Number	60809-0146-US

U.S. PATENT DOCUMENTS

Examiner Initials	Cite No.	Document Number Number - Kind Code ¹	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Class	Subclass	Filing Date if Appropriate
/MC/		5,268,639	07/07/1993	Gasbarro			
		5,335,206	08/02/1994	Kawamoto			
		5,359,722	10/25/1994	Chan			
		5,384,737	01/24/1995	Childs et al.	365	189.05	
		5,455,923	10/03/1995	Kaplinsky			
		5,457,790	10/10/1995	Iwamura			
		5,481,731	01/02/1996	Conary			
		5,498,990	03/12/1996	Leung			
		5,524,249	06/04/1996	Suboh			
		5,585,745	12/17/1996	Simmons et al.	326	93	
		5,586,332	12/17/1996	Jain			
		5,615,169	03/25/1997	Leung	365	233.5	
		5,629,897	05/13/1997	Iwamoto			
		5,655,113	08/05/1997	Leung et al.	395	552	
		5,666,322	09/09/1997	Conkle			
		5,666,324	09/09/1997	Kosugi			
		5,673,398	09/30/1997	Takeda			
		5,677,849	10/14/1997	Smith			
		5,687,183	11/11/1997	Chesley			
		5,696,729	12/09/1997	Kitamura			
		5,742,194	04/21/1998	Saeki	327	298	
		5,754,838	05/19/1998	Shibata et al.	395	559	
		5,768,213	06/16/1998	Jung			
		5,790,839	08/04/1998	Luk			
		5,796,673	08/18/1998	Foss			
		5,815,462	09/29/1998	Konishi			
		5,822,255	10/13/1998	Uchida	365	194	
		5,860,125	01/12/1999	Reents			
		5,880,998	03/09/1999	Tanimura			
		5,886,946	03/23/1999	Ooishi	365	233	
		5,910,930	06/08/1999	Dieffenderfer	368	156	
		5,917,760	06/29/1999	Millar			

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/MC/		5,930,197	07/27/1999	Ishibashi			
↓		5,955,904	09/21/1999	Kawasaki	327	156	
		6,014,339	01/11/2000	Kobayashi	365	233	
		6,088,774	07/11/2000	Gillingham			
		6,128,700	10/03/2000	Hsu			
		6,226,723	05/01/2001	Gustavson			
↓		6,259,288	07/10/2001	Nishimura	327	156	
		6,442,644	08/27/2002	Gustavson et al.	711	105	

FOREIGN PATENT DOCUMENTS

Examiner Initials	Cite No.	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Class	Sub-class	Translation	
		Country Code ² - Number ³ - Kind Code ⁴ (if known)					Yes	No

OTHER NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published
/MC/		BURSKY, "Memory-CPU Interface Speeds Up Data Transfers"
		CHOI, "16Mbit Synchronous DRAM with 125Mbyte/sec Data Rate".
		GARY et al., "The PowerPC™ 603 Microprocessor: A Low-Power Design for Portable Applications"
		GILLINGHAM, "SLDRAM Architectural and Functional Overview," SLDRAM Consortium, August 29, 1997.
		HAN, "Skew Minimization Techniques for 256M-bit Synchronous DRAM and beyond," 1996 Symposium on VLSI Circuits Digest of Technical Papers.
		HOROWITZ, "PLL Design for a 500 MB/s Interface," 1993 IEEE International Solid-State Circuits Conference, Digest of Technical Papers, February 25, 1993.
		KODAMA, "A 150-MHz 4-Bank 64M-bit SDRAM with Address Incrementing Pipeline Scheme," 1994 Symposium on VLSI Circuits Digest of Technical Papers, 1994
		KUSHIYAMA, "A 500-Megabyte/s Data-Rate 4.5M DRAM," IEEE Journal of Solid-State Circuits, Vol. 28 No. 4, April 1993
↓		KUSHIYAMA, "500Mbyte/sec Data-Rate 512 Kbits x 9 DRAM Using a Novel I/O Interface," 1992 Symposium on VLSI Circuits Digest of Technical Papers, 1992
		LEE et al., "A 2.5 V CMOS Delay-Locked Loop for an 18 Mbit, 500 Megabyte/s DRAM"

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		LINES et al., "High Speed Circuit Techniques in a 150Hz 64M SDRAM"
/MC/		NICOL, "A Scalable Pipelined Architecture for Fast Buffer SRAM's," IEEE Journal of Solid-State Circuits, Vol. 31, No. 3, March 1996.
		SAEKI et al., "A2.5ns Clock Access, 250MHz 256Mb SDRAM with Synchronous Mirror Delay,"
		SAKAI, "A Synchronous DRAM with New High-Speed I/O Lines Method for the Multimedia Age," IEICI Trans. Electron., Vol. E78-C, No. 7, July, 1995.
		TAKAI, "250 Mbyte/s Synchronous DRAM Using a 3-Stage-Pipelined Architecture," IEEE Journal of Solid-State Circuits, Vol. 29, No. 4, April, 1994.
		YOO, "A 150HMz 8-Banks 256M Synchronous DRAM with Wave Pipelining Methods," 1995 IEEE International Solid-State Circuits Conference, Digest of Technical Papers, February 17, 1995.
		IEEE, "Draft Standard for A High-Speed Memory Interface (SyncLink)", 1996.
		"IEEE Standard for High-Bandwidth Memory Interface Based on Scalable Coherent Interface SCI Signaling Technology (RamLink)" 1996
		MoSys, Inc., "MD904 to MD920, ½ to 2 ½ MByte Multibank® DRAM (DRAM9®)" MoSys Data Sheet, February 21, 1997
		Rambus, Inc., "NEC 18Mb Rambus DRAM Core Interface," August 9, 1992.
		Rambus Inc., "p5 18Mb RDRAM Test Modes," August 3, 1992.
		Rambus, Inc., "Rambus Interface Technical Description" January 20, 1992
		Rambus, Inc., "Rambus Interface Technical Description," August 18, 1992.
		Rambus, Inc., "RDRAM Design Guide," April 20, 1995.
		Rambus Inc, 8/9-Mbit (1M x 8/9) and 16/18 (2M x 8/9) RDRAM Data Sheet, March 1996.
		Rambus Inc, 16/18Mbit (2Mx8/9) & 64/72Mbit (8Mx8/9) Concurrent RDRAM Data Sheet, July 1996.
		SLDRAM Consortium Data Sheet, "400 Mb/s/pin SLDRAM Advance Data Sheet," August 27, 1997.
		SyncLink Consortium, "SyncLink Feasibility v1.1," April 30, 1996.

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